PTO/SB/21 (09-04)

Approved for use through 07/31/2006. OMB 0651-0031
U.S. Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

	Application Number 10/813,907							
O TRANSMITTAL	Filing Date		March 30, 2004					
RORM	First Named Inventor		Christopher J. Diorio					
1 0 2005	Art Unit		2818					
To be used for all Orrespondence after	initial filing)	Examiner Name	_	Unassigned				
Total Number ages in This Submiss		Attorney Docket N	umber	IMPJ-0027A				
	ENCLOSURES (check all that apply)							
Fee Transmittal Form	Drawing(s)			After Allowance Communication to TC				
Fee Attached	Licensing-	related Papers		Appeal Communication to Board of Appeals and Interferences				
Amendment / Reply	Petition			Appeal Communication to TC (Appeal Notice, Brief, Reply Brief)				
After Final	Provisiona	Convert to a al Application		Proprietary Information				
Affidavits/declaration(s)		Attorney, Revocation of Correspondence Add	ress	Status Letter				
Extension of Time Request	Terminal Disclaimer			Other Enclosure(s) (please identify below):				
Express Abandonment Request	Request fo	or Refund	į					
Information Disclosure Statement	☐ Lan	dscape Table on CD						
Certified Copy of Priority Document(s)	Priority Remarks							
Reply to Missing Parts/								
Incomplete Application								
under 37 CFR1.52 or 1.53	Reply to Missing Parts under 37 CFR1.52 or 1.53							
SIGN	NATURE OF A	APPLICANT, ATTOR	RNEY. O	R AGENT				
Firm	THELEN REID 8		,					
Signature								
Printed Name	David B. Ritchie							
Date	2/41.		Reg. No.	31,562				
CERTIFICATE OF TRANSMISSION/MAILING								
I hereby certify that this correspondence is being facsimile transmitted to the USPTO or deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date shown below.								
Signature Plather (DVDII)								
				r that a real				

This collection of information is required by 37 CFR 1.5. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11 and 1.14. This collection is estimated to 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Date

Typed or printed name

Beatrice Orozco

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT:

Christopher J. Diorio et al.

SERIAL NO.:

10/813,907

FILING DATE:

March 30, 2004

TITLE:

Rewriteable Electronic Fuses

EXAMINER:

Unassigned

ART UNIT:

2818

CERTIFICATE OF MAILING

I hereby certify that this paper is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: MAIL STOP: Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date printed below:

MAIL STOP: AMENDMENT **COMMISSIONER FOR PATENTS**

P.O. Box 1450

ALEXANDRIA, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Each item of information listed in the attached FORM PTO-1449, for which a copy of each is attached (unless the blanket waiver referred to below applies), may be material to the examination of the above-identified application and is, therefore, submitted in compliance with the duty of disclosure defined in 37 CFR §§ 1.56, 1.97 and 1.98. The Examiner is requested to review, consider and document each such item in the official record of this application.

Note: If this box is checked, this case was filed after June 30, 2003 and qualifies for the blanket waiver of deposit of copies of U.S. Patents and U.S. Patent Application

Publications in accordance with the written waiver of 37 CFR §1.98 (a)(2)(i) dated July

11, 2003. Accordingly, such copies are not attached.

This Information Disclosure Statement under 37 CFR §§ 1.56, 1.97 and 1.98 is not to be construed as a representation that a search has been made, that additional information material to the examination of this application does not exist, or that any one or more of these items constitutes prior art.

Ι

This statement is filed pursuant to (CHECK ONE BOX):

37 C.F.R. § 1.97(b).

This information disclosure statement is filed either:

- (1) within three months of the filing date of a national application other than a continued prosecution application under §1.53(d);
- (2) within three months of the date of entry of the national stage as set forth in 37 C.F.R. §1.491 in an international application;
 - (3) before the mailing date of a first office action on the merits; or
- (4) before the mailing of a first office action after the filing of a Request for Continued Examination under 37 C.F.R. §1.114, whichever event occurs last.

Accordingly, this information disclosure statement requires no fee and no certification.

	37 C.F.R.	§ 1.97(c).
--	-----------	------------

This information disclosure statement is filed after the period specified in 37 C.F.R. § 1.97(b), but before the mailing date of any of the following:

- (1) a final action under 37 C.F.R. § 1.113;
- (2) a notice of allowance under 37 C.F.R. § 1.311; or
- (3) an action that otherwise closes prosecution in the application.

Accordingly, this information disclosure statement requires either:

- (1) the fee specified in 37 C.F.R. § 1.17(p) for submission of an information disclosure statement under 37 C.F.R. § 1.97(c); or
 - (2) a certification according to 37 C.F.R. § 1.97 (e)(1) or (2).

37 C.F.R. § 1.97(d).

This information disclosure statement is filed after the period specified in 37 C.F.R. § 1.97 (c).

Accordingly, this information disclosure statement requires:

(1) a certification in accordance with 37 C.F.R. § 1.97(e); and

(2) the fee specified in 37 C.F.R. § 1.17 (p) to consider an information disclosure statement under 37 C.F.R. § 1.97(d).

If this statement crosses in the mail with an office action, or is otherwise not in the indicated category of 37 C.F.R. § 1.97, it is respectfully requested that this statement be treated in the next appropriate category and made of record. To the extent required, please treat this paper as a conditional petition for acceptance of the information disclosure statement.

	П						
Fees Due (C	HECK ONE BOX):						
\boxtimes	No fee is due.						
	The fee specified in 37 C.F.R. § 1.17(p) for submission of an information disclosure statement under 37 C.F.R. § 1.97(c) or 37 C.F.R. § 1.97(d) is enclosed (\$180).						
	m						
Certification	(CHECK ONE BOX):						
\boxtimes	No certification is necessary.						
	Pursuant to 37 C.F.R. § 1.97(e)(1), the undersigned hereby certifies: That each item of information contained in this information disclosure statement was first cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this information disclosure statement.						
	Pursuant to 37 C.F.R. § 1.97(e)(2), the undersigned hereby certifies: No item of information contained in this information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the undersigned after making reasonable inquiry, no item of information contained in this information disclosure statement was known to any individual designated in 37 C.F.R. § 1.56(c) more than three months prior to the filing of this information disclosure statement.						

IV

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Please charge any additional required fee or credit any overpayment to our deposit account number 50-1698.

Dated: February ________, 2005

Respectfully submitted, THELEN REID & PRIEST LLP

David B. Ritchie Reg. No. 31,562

THELEN REID & PRIEST LLP P.O. Box 640640 San Jose, CA 95164-0640 (408) 292-5800 direct dial (408) 287-8040 direct fax

Form PTO 1449 (Rev. 2-32) U.S. Department of Commerce Patent and Trademark Office				IMPJ-00		Serial No. 10/813,907			
	Info	mation Disclosure		MARKARET .	Applica	nt: Christopher	J. Diorio et al	l .	
(Use several sheets if necessary)				Filed: N	March 30, 2004	Group: 28	Group: 2818		
				U.C. D. A. A.D.					
Init.	T	Document No.	Date	U.S. Patent Docume Name	nts	Class	Subclass	Filing	Date
	A	5,736,764	4/17/1998	Chang				 	
-	В	5,777,926	7/7/1998	Trinh et al.					
	$\frac{1}{C}$	5,801,994	9/1/1998	Chang et al.				 	
	D	5,841,165	11/24/1998	Chang et al.				-	
	E	5,901,084	5/4/1999	Ohnakado					
	F	5,912,842	6/15/1999	Chang et al.					
	G	5,966,329	10/12/1999	Hsu et al.					
	Н	5,982,669	11/6/1999	Kalnitsky et al.					
	I	6,055,185	4/25/2000	Kalnitsky et al.				<u> </u>	
	J	6,137,723	10/24/2000	Bergemont et al.					
	K	6,384,451	5/7/2002	Caywood					
						-			
						-			
							-		
, <u>-</u>									
				Ei D	-	•			
				Foreign Document	.5			Tran	slation
Init.		Document No.	Date	Country	-	Class	Subclass		No
	<u> </u>								
	L	Other Do International Sear	cuments (Inclue	ding Author, Title, I ication No.: PCT/US	Date, Pert 3 03/31792	inent Pages, et . date of mailin	c.) g August 12. 2	2004.	
	M	í							<u>—</u> —
	M Witters, et al., "Analysis and Modeling of On-Chip High-Voltage Generator Circuits for Use in EEPROM Circuits", IEEE Journal of Solid-State Circuits, VOL. 24, No. 5, October 1989, pp. 1372-1380.								
	<u> </u>								
<u> </u>	<u> </u>					Data Carraid			
Examiner					Date Considered				
Examiner	: Initia	al if citation considerance and not considerance	red, whether or i lered. Include a	not citation is in conf copy of this form wit	ormance with the next	rith MPEP 609; communication	Draw line thre to applicant.	ough cit	ation